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| Application Number | 10/789,697 |
| Filing Date | 02/27/2004 |
| First Named Inventor | Pepper |
| Art Unit | 2857 2863 |
| Examiner Name | West LE |
| Attorney Docket Number | 1004-P03072US |

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U. S. PATENT DOCUMENTS

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| Examiner Signature | <i>John Lee</i> | Date Considered | <i>07/19/2005</i> |
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| INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary) | | | | Complete if Known | | |
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| | | | | Examiner Name | LE | |
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| NON PATENT LITERATURE DOCUMENTS | | | |
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| Examiner Initials* | Cite No. ¹ | Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published. | T ² |
| JL | | PacBERT Bit Error %Rate Test (BERT) Test Application; December 2001; Spirent Communications | |
| | | Verification of Skew and Jitter Tolerance and Compensation in High-Speed Interfaces; Alex Genusov, Zaiq Technologies; Steve Callahan, Zaiq Technologies | |
| | | Serdes Framer Interface Level 5 (SFI-5): Implementation Agreement for 4-Gb/s Interface for Physical Layer Devices; Peter Dartnell, Mike Lerer, Jeffrey Lynch | |
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